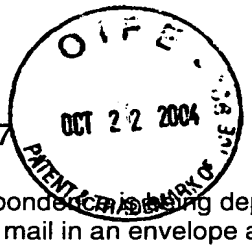
IFW



Docket No.: S&ZIO020307

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:

By: _____ Date: October 19, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.	:	10/680,383	Confirmation No: 5862
Applicant	:	Ronald Kakoschke et al.	
Filed	:	October 6, 2003	
Title	:	Memory Cell Array Comprising Individually Addressable Memory Cell and Method of Making the same	
Art Unit	:	2818	
Examiner	:	Michael Thanh Tran	
Docket No.	:	S&ZIO020307	
Customer No.	:	24131	

RESPONSE

Hon. Commissioner for Patents

S i r:

Responsive to the Office Action dated September 21, 2004, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-9, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-9 is solicited.

Respectfully submitted,

For Applicants

WERNER H. STEMER
REG. NO. 34,956

Date: October 19, 2004

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